substantially circular contour portion surrounding said main surface, a curved positioning notch formed in said circular contour portion and connecting portions defined between said circular contour portion and said curved positioning notch;

forming a photoresist film for a photolithographic process on said surface of said wafer; and

forming patterns on said surface of said wafer,

wherein an outer peripheral part of said wafer is chamfered in a thickness direction by mechanical chamfering, and

wherein said connecting portions are chamfered in a plane parallel to said main surface by mechanical chamfering.

A process for producing a semiconductor device according to claim 14, further comprising vapor-growing a film on the wafer surface.

A process for producing a semiconductor device according to claim 15, further comprising transporting said wafer.

A process for producing a semiconductor device according to claim by, wherein said provided wafer is finished in a mirror wafer state.

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A process for producing a semiconductor device according to claim wherein said curved positioning notch does not divide the wafer into several sections.

A process for producing a semiconductor device according to claim b, wherein said curved positioning notch does not cause loss of wafer structurable area for electronic components.

A process for producing a semiconductor device according to claim 4, wherein said curved positioning notch does not divide the wafer into several sections.

A process for producing a semiconductor device according to claim 14, wherein said curved positioning notch does not cause loss of wafer structurable area for electronic components.

A process for producing a semiconductor device according to claim 14, further comprising positioning said wafer by rotating said wafer.

A process for producing a semiconductor device according to claim 2, further comprising positioning said wafer by optical means.

A process for producing a semiconductor device according to claim 14, further comprising transporting said wafer.

A process for producing a semiconductor device according to claim further comprising the step of diffusion.

A process for producing a semiconductor device according to claim 4, further comprising the step of etching.

27. A process for producing a semiconductor device according to claim 14, wherein said provided wafer is finished in a mirror wafer state.

28. A process for producing a semiconductor device, comprising:
providing wafer for forming an integrated circuit thereon, the wafer
having a main surface on which an integrated circuit is to be formed, a
substantially circular contour portion surrounding said main surface, a curved
positioning notch formed in said circular contour portion and connecting
portions defined between said circular contour portion and said curved
positioning notch;

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forming a photoresist film for a photolithographic process on said surface of said wafer; and

forming patterns on said surface of said wafer,

wherein an outer peripheral part of said wafer is chamfered in a thickness direction by grindstone, and

wherein said connecting portions are chamfered in a plane parallel to said main surface by grindstone.

A process for producing a semiconductor device according to claim 8, further comprising vapor-growing a film on the wafer surface.

A process for producing a semiconductor device according to claim 79, further comprising transporting said wafer.

A process for producing a semiconductor device according to claim, wherein said provided wafer is finished in a mirror wafer state.

A process for producing a semiconductor device according to claim 79, wherein said curved positioning notch does not divide the wafer into several sections.

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A process for producing a semiconductor device according to claim 29, wherein said curved positioning notch does not cause loss of wafer structurable area for electronic components.

A process for producing a semiconductor device according to claim 2/8, wherein said curved positioning notch does not divide the wafer into several sections.

A process for producing a semiconductor device according to claim 28, wherein said curved positioning notch does not cause loss of wafer structurable area for electronic components.

A process for producing a semiconductor device according to claim 28, further comprising positioning said wafer by rotating said wafer.

A process for producing a semiconductor device according to claim 36, further comprising positioning said wafer by optical means.

A process for producing a semiconductor device according to claim 28, further comprising transporting said wafer.

A process for producing a semiconductor device according to claim further comprising the step of diffusion.

A process for producing a semiconductor device according to claim further comprising the step of etching.

A process for producing a semiconductor device according to claim , wherein said provided wafer is finished in a mirror wafer state.

A process for producing a semiconductor device comprising:

providing a wafer for forming an integrated circuit thereon, the wafer having a main surface on which an integrated circuit is to be formed, a substantially circular contour portion surrounding said main surface, a curved positioning notch formed in said circular contour portion and connecting portions defined between said circular contour portion and said curved positioning notch, wherein said connecting portions are chamfered in a plane parallel to said main surface:

forming a photoresist film for a photolithographic process on said surface of said wafer; and

forming patterns on said surface of said wafer,

wherein said curved positioning notch does not cause loss of wafer structurable area for electronic components.

43. A process for producing a semiconductor device comprising:

providing a wafer for forming an integrated circuit thereon, the wafer having a main surface on which an integrated circuit is to be formed, a substantially circular contour portion surrounding said main surface, a curved positioning notch formed in said circular contour portion and connecting portions defined between said circular contour portion and said curved positioning notch, wherein said connecting portions are chamfered in a plane parallel to said main surface;

forming a photoresist film for a photolithographic process on said surface of said wafer; and

forming patterns on said surface of said wafer,

wherein said curved positioning notch does not divide the wafer into several sections.

A process for producing a semiconductor device according to claim 4B, wherein said curved positioning notch does not cause loss of wafer structurable area for electronic components.

Please charge any additional fees due in connection with the filing of this paper or credit any overpayment in this regard to the undersigned's deposit account number, Account No. 01-2135 (ref.: 501.20289RC6).

Respectfully submitted,

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